PTO/SB/08a (06-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

substitute for form 1449A/PTO

	Complete if Known
Application Number	10/728,234
Filing Date	December 4, 2003
First Named Inventor	Dan TOMESCU
Art Unit	2183
Examiner Name	
	205-2004

(Use as many sheets as necessary)
Sheet 1 of 3

Attorney Docket Number | 5227-0001-1

			U. S. PATENT		
Examiner nitials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
illuais	140.	Number-Kind Code ^{2 (4 known)}	MIMP DO-1111	Applicant of Cheo Document	Figures Appear
DB		^{US-} 4575818	03-11-1986	Almy, et al.	
		US- 4907148	03-06-1990	Morton	
		^{US-} 4983958	01-08-1991	Carrick	
		US- 5122984	06-16-1992	Strehler	
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V		^{US-} 6295534	09-25-2001	Mann	
DB		^{US-} 6317819	11-13-2001	Morton	

	FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ Number ⁴ 'Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Τ ⁶			

Examiner Signature	/Dennis B	Butler/	Date Considered	04/24/2006

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Examiner Signature	/Dennis Butler/	Date Considered	04/24/2006

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Substitute fo	or form 1449B/PTO			Complete if Known				
				Application Number	10/728,234			
	RMATION DI			Filing Date	December 4, 2003			
STAT	TEMENT BY	APPLI	CANT	First Named Inventor Dan TOMESCU				
				Art Unit	2183			
	(Use as many sheets as necessary)			Examiner Name				
Sheet	3	of	3	Attorney Docket Number	5227-0001-1			

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
DB		MITU et al., "A CMOS Implementation of a Connex Memory," pp 579-582, IEEE, 1997.	
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Examine	Date	04/24/2006
Signature	Considered	

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Complete if Known				
Application Number	10/728,234			
Filing Date	December 4, 2003			
First Named Inventor	DAN TOMESCU			
Art Unit	2183			
Examiner Name				
Attorney Docket Number	5227-0001-1			

			U. S. PATEN	T DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
DB		^{US-} 5,758,176	05-26-1998	AGARWAL	
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	Country Code ³ "Number ⁴ " Kind Code ³ (if known) MM-DD-YYYY Or Relevant Figures	Or Relevant Figures Appear	T⁵			
DB		WO 01/53933	07-26-2001	MICRON TECHNOLOGY		
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Examiner Signature	/Dennis Butler/	Date Considered	04/24/2006

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	e for form 1449/PTO					Complete if Known
					Application Number	10/728,234
INFO	DRMATION	DIS	CLOS	SURE	Filing Date	December 4, 2003
STA	TEMENT B	BY A	Application Number 10/728,234 Filing Date December 4, 2003 First Named Inventor DAN TOMESCU Art Unit 2183 Examiner Name			
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Sheet	2	of	2		Attorney Docket Number	5227-0002-1

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
DB		SHAW D E et al: "Active Memory Chip: A Brief Survey And Case Study" Circuits, Systems And Signal Processing, Cambridge, MS, US, vol. 6 no. 2, 1987 pages 239-258, XP000747239,	
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Examiner Signature	/Dennis Butler/	Date Considered	04/24/2006

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Col	nplete if Known	
Application Number	10/728,234	
Filing Date .	December 4, 2003	
First Named Inventor	Gheorghe Stefan	
Art Unit	2183	
Examiner Name		
Attorney Docket Number	5227-0001-1	

			U. S. PATENT	DOCUMENTS	
Examiner Cite Initiats* No.1		Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevan Figures Appear
DB		^{US-} 4780811	10-25-1988	AOYAMA et al.	
DB		^{US-} 6145075	11-07-2000	HENRY et al.	
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DB	WO 01/33352 A1	05/10/2001	Advanced Micro Devices		
DB	GB 2230119 A	10/10/1990	Intel Corporation		
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Initials*	No. ¹	the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DB		INTEL, Intel Architecture Software Developer's Manual - Vol. 1: Basic Architecture, Chpt. 4, "Procedure Calls, Interrupts, and Exceptions"	
DB		SHAW, David E. et al., Active Memory Chips: A Brief Study and Case Study", Circuits Systems Signal Process, Vol. 6, NO. 2, 1987	
DB		BERRACHED, Ali et al., A Decoupled Access/Execute Architecture for Efficient Access of Structured Data, PA State University, 1993 IEEE	
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